Notice of Allowability	Application No.	Applicant(s)	
	09/708,494	NSAME, PASCAL A	٨.
	Examiner	Art Unit	
	Kim T. Huynh	2112	
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet w (OR REMAINS) CLOSED in or other appropriate common GHTS. This application is	n this application. If not include nunication will be mailed in due	ed course. THIS
1. This communication is responsive to 1/13/05.			
2. The allowed claim(s) is/are <u>1-10, 12-20, 22-29</u> .			
3. \boxtimes The drawings filed on <u>09 November 2000</u> are accepted by	the Examiner.		
4. ☐ Acknowledgment is made of a claim for foreign priority una) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received:	been received. been received in Applicati	on No	ion from the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONV THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the rec	_l uirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give			OTICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in to 7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT.	son's Patent Drawing Revie s Amendment / Comment on .84(c)) should be written on the header according to 37 C sit of BIOLOGICAL MAT	or in the Office action of the drawings in the front (not the FR 1.121(d). ERIAL must be submitted. N	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date 4113 65 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview S Paper No 08), 7. Examiner's	nformal Patent Application (PTC Summary (PTO-413), ./Mail Dates s Amendment/Comment s Statement of Reasons for Allo	
		TIMVO	MINER

Application/Control Number: 09/708,494 Page 2

Art Unit: 2112

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Frederick Gibb, 37,629 on 4/5/05.

The application has been amended as following:

As per claim 10, a system-on-a-chip integrated circuit structure comprising:

- A bridge having a plurality of channels;
- At least one bus connected to a unique dedicated channel in said bridge;
- At least one memory unit connected to a unique dedicated channel in said bridge; and
- At least one input/output unit connected to a unique dedicated channel in said bridge;
- Wherein said at least one bus includes;
 - o A processor local bus connected to said bridge, wherein said bridge includes a first channel dedicated to said processor local bus; and

Application/Control Number: 09/708,494 . Page 3

Art Unit: 2112

 A peripheral device bus connected to said bridge, wherein said bridge includes a second channel dedicated to said peripheral device bus, wherein said structure further comprises:

- at least one logic device connected to said processor local bus;
 and
- at least one peripheral device connected to said peripheral device bus.

As per claim 20, a bridge for a system-on-a-chip(SoC) integrated circuit structure comprising:

- o A plurality of dedicated channels in said bridge each uniquely connected to one or more of:
- At least one bus within said SoC;
- At least one memory unit within said SoC;
- At least one input/output unit within said SoC; and
- At least one peripheral device within said SoC;
- o Wherein said at least one bus includes:
 - o A processor local bus connected to said bridge, wherein said bridge includes a first channel dedicated to said processor local bus; and
 - A peripheral device bus connected to said bridge, wherein said bridge includes a second channel dedicated to said peripheral device bus, wherein said SoC includes:

Application/Control Number: 09/708,494 Page 4

Art Unit: 2112

At least one logic device connected to said processor local bus;

and

o At least one peripheral device connected to said peripheral device

bus.

As per claims 11 and 21 are cancelled.

Conclusion

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kim. Huynh whose telephone number is (571)272-3635 or via e-mail addressed to [kim.huynh3@uspto.gov]. The examiner can normally be reached on M-F 9.00AM- 6:00PM. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached at (571)272-3632 or via e-mail addressed to [mark.Rinehart@uspto.gov].

The fax phone numbers for the organization where this application or proceeding is assigned are (703)872-9306 for regular communications and After Final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571)272-2100.

Kim Huynh

April 5, 2005

TIM VO PRIMARY EXAMIN'